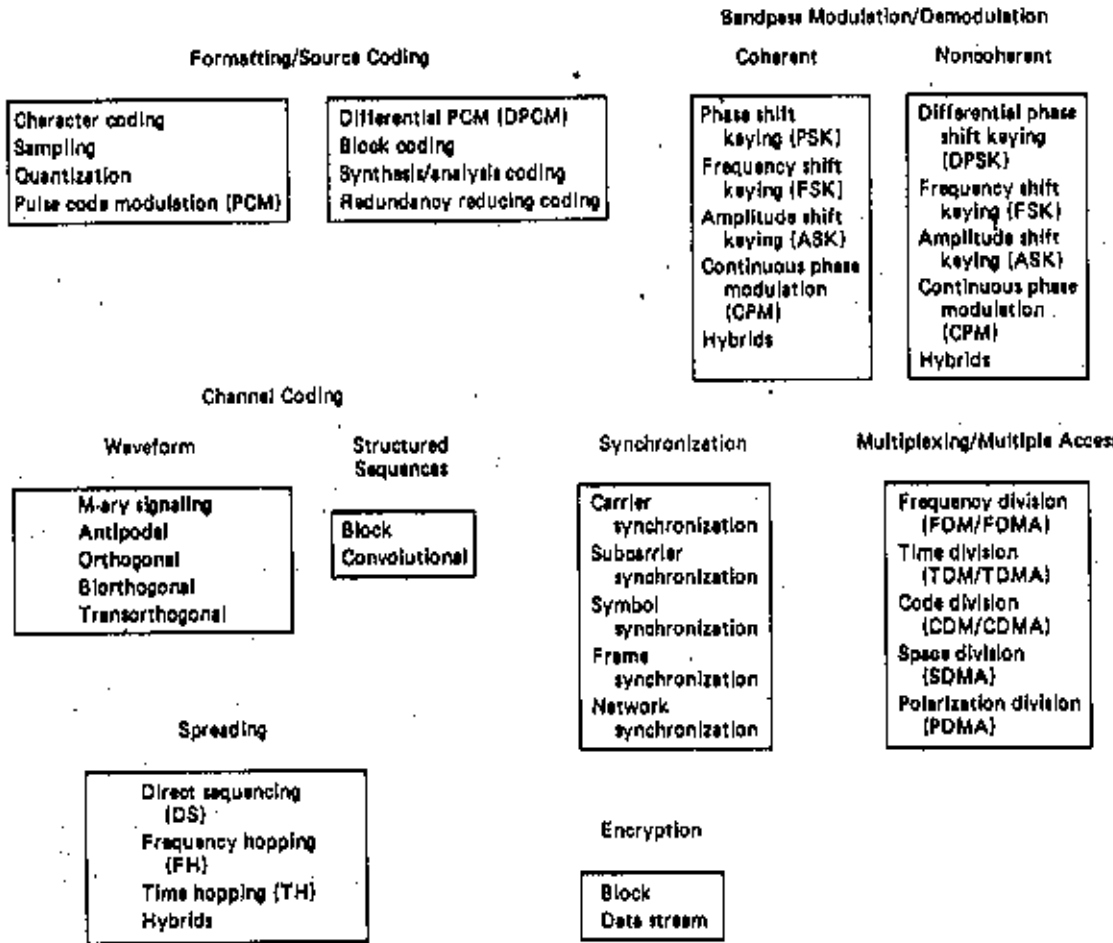
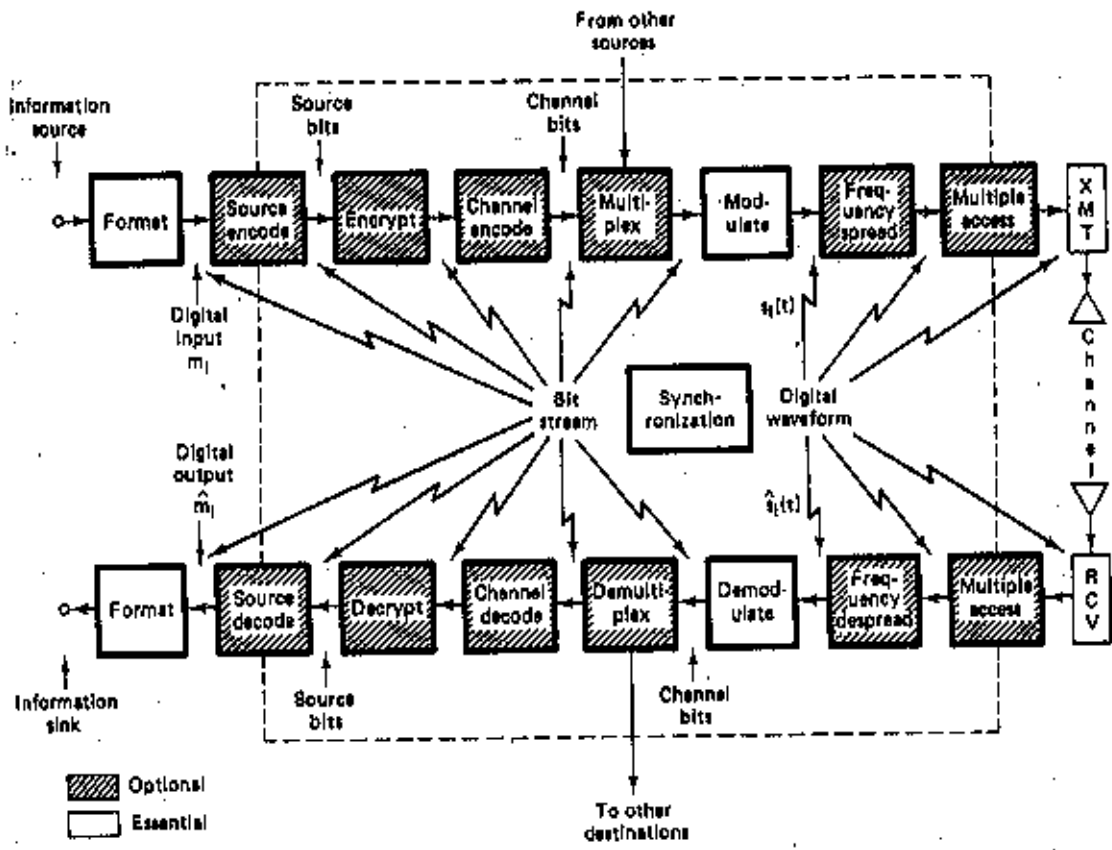


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LECTURE #5

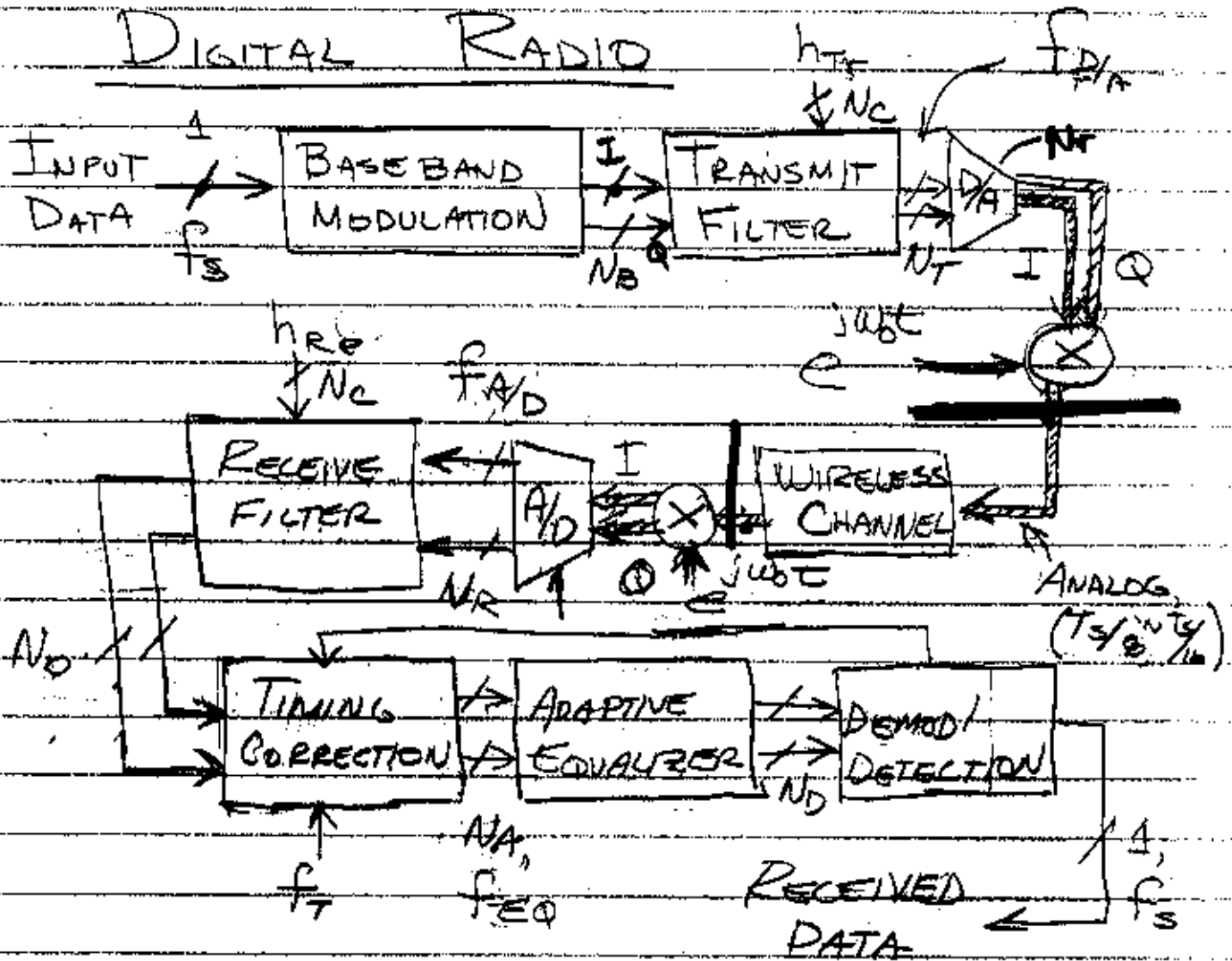
INTRO TO RADIO SYSTEMS

READINGS SKLAR 1 & SKLAR 2



FIRST VERSION

DIGITAL RADIO



DESIGN PROCEDURE:

ASSUME: MODULATION, f_s , BANDWIDTH SPECIFIED

1) ALGORITHM DESIGN:

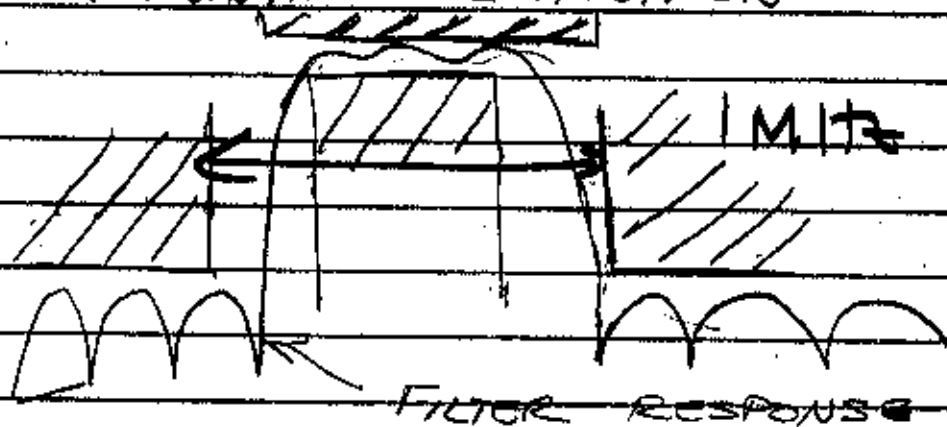
- 1) TRANSMIT / RECEIVE FILTERS
- 2) MODULATOR
- 3) DEMODULATOR
- 4) DETECTOR
- 5) TIMING CORRECTION
- 6) ADAPTIVE - EQUALIZER

DESIGN PROCEDURES (CONT.) ②

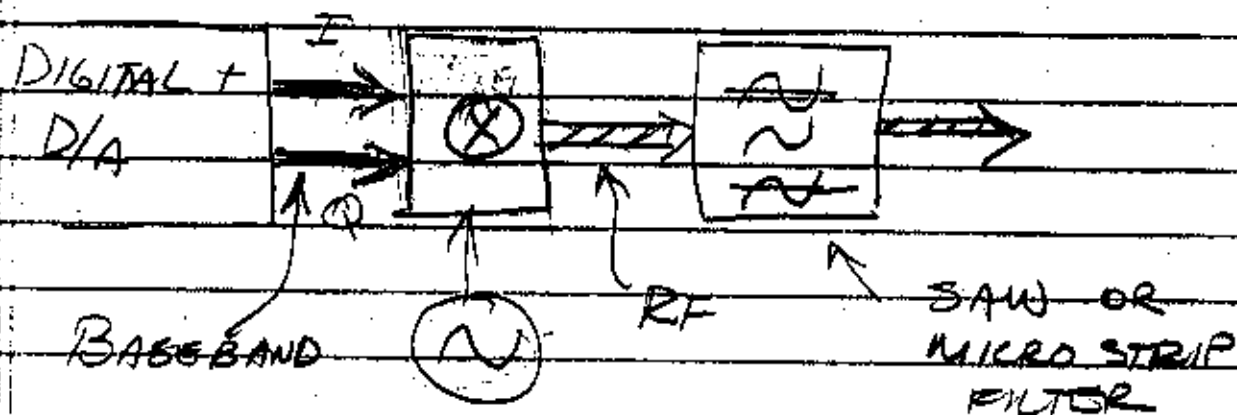
- 2) IMPLEMENTATION ARCHITECTURE
- 3) WORD LENGTH OPTIMIZATION
- 4) HARDWARE MAPPING

TRANSMIT/RECEIVE FILTERS

BANDWIDTH SPECIFICATION



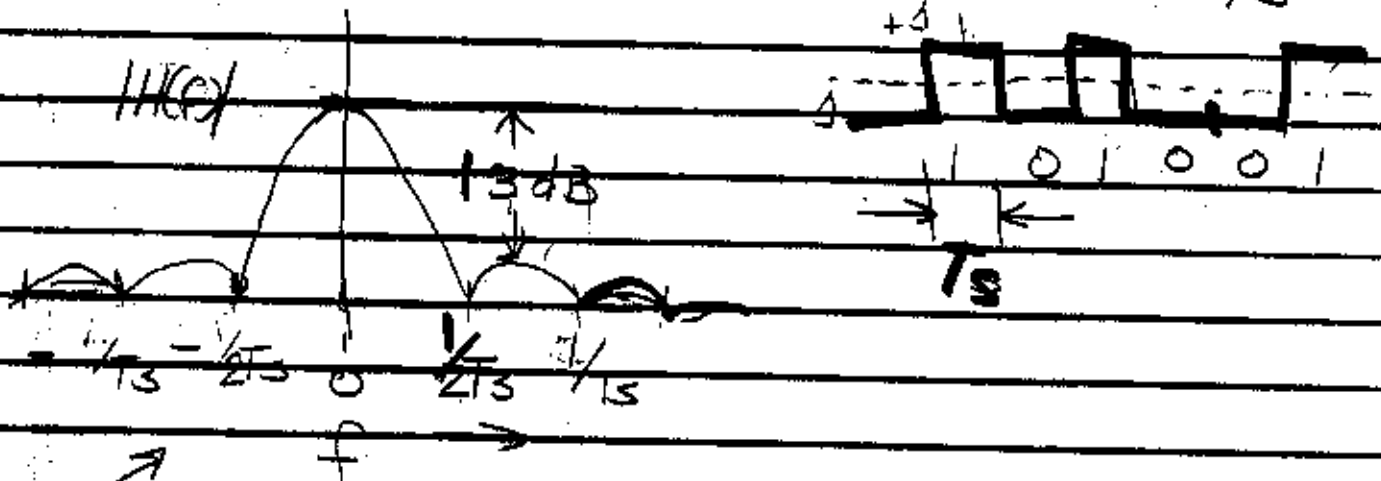
ACTUALLY A CONCATENATION OF THE DIGITAL T/R FILTERS & RF FILTERS



FILTER DESIGN

(3)

SYMBOL RATE = f_s BITS/SEC. = $\frac{1}{T_s}$

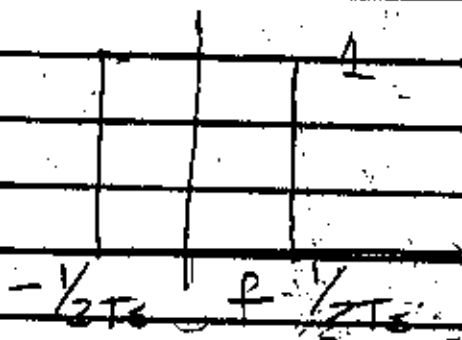


FREQ. RESPONSE OF 1 PULSE (REAL)

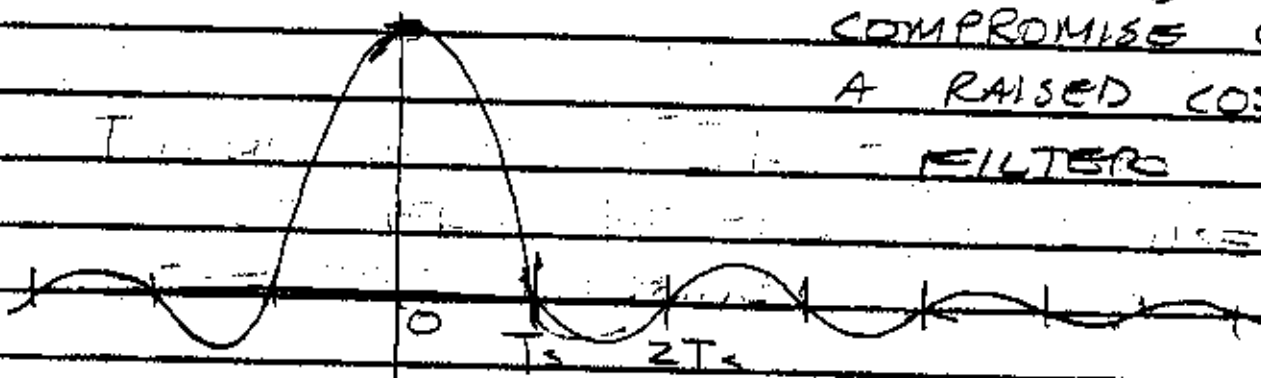
SAMPLE RATE = $\frac{1}{T_s}$

BASEBAND BW = $\frac{f_s}{2}$; PASSBAND BW = f_s

IF WE BANDLIMIT TO THE MINIMUM POSSIBLE AMOUNT $\frac{1}{2T_s}$



THEN THE TIME RESPONSE GOES ON FOREVER, SO WE COMPROMISE WITH A RAISED COSINE FILTERS



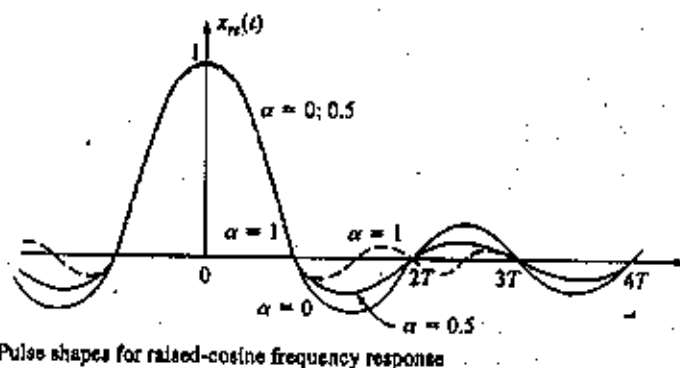
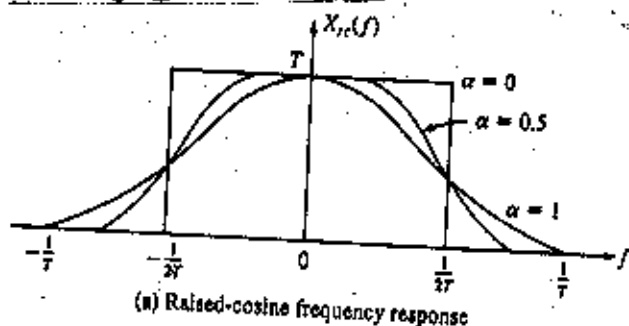
RAISED COSINE FILTER

④

FREQUENCY RESPONSE

$$H_{RC}(f) = \begin{cases} 1 & ; |f| \leq \frac{(1-\alpha)}{2T_s} \\ \frac{T_s}{2} \left[1 + \cos \frac{\pi T_s}{\alpha} \left(|f| - \frac{(1-\alpha)}{2T_s} \right) \right] & ; \frac{1-\alpha}{2T_s} < |f| \leq \frac{1+\alpha}{2T_s} \\ 0 & ; |f| > \frac{1+\alpha}{2T_s} \end{cases}$$

NO ISI WITH THIS FILTER



NORMALLY WE SPLIT THE FILTER BETWEEN THE TRANSMIT & RECEIVE

$$H_{RC}(f) = H_T(f) H_R(f)$$

$$H_T(f) = H_R(f) = \sqrt{H_{RC}(f)}$$

RAISED COSINE FILTERS (CONT.)

TO GET THE SQUARE ROOT FILTER IMPULSE RESPONSE WE CHOOSE A SAMPLE RATE FOR THE FILTER WHICH WILL BE THE SAME AS THE A/D & D/A SAMPLE RATES, SO IF WE LET $f_{s/4} = f_{s/4} = 4/T_s$ (4 TIMES THE ABSOLUTE MINIMUM).

WE GET: $m = \frac{(N-1)}{2}$

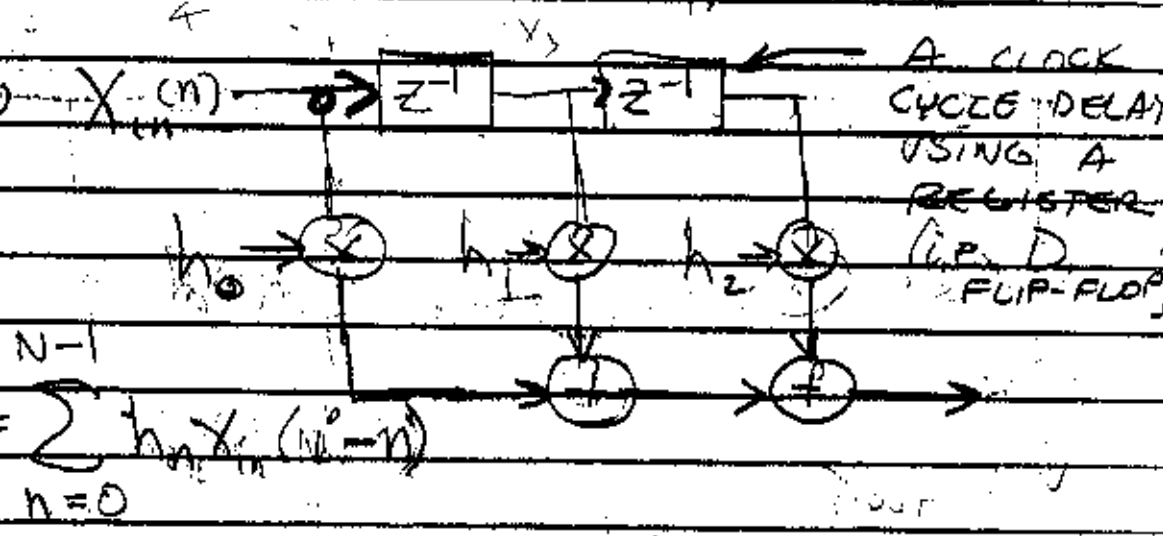
$$h_{FR}(n) = \sum_{m = -(N-1)/2}^{(N-1)/2} \sqrt{H_{RC}\left(\frac{4m}{NT_s}\right)} e^{j \frac{2\pi mn}{N}}$$

$-\frac{(N-1)}{2} \leq n \leq \frac{N-1}{2}$

NOW TO IMPLEMENT THE FILTER:

FIR $N=3$ (AP) $h_{FR}(n) \equiv h_{FR}(n)$

DRAW THE SIGNAL FLOW GRAPH



$$Y_{out}(z) = \sum_{n=0}^{N-1} h_n X_{in}(z^{-(N-n)})$$