

flowchart is similar to that given in Fig.3.

6. CONCLUSIONS

The data signal generation from stored elements avoids multiplications and permit a high processing speed.

The memory size is small, with a proper choice of f_s/f_b ratio. Hence, the method is useful in implementation of modems.

REFERENCES

- /1/ M.F.Choquet and H.J.Nussbaumer, "Generation of synchronous data transmission signals by digital echo modulation", IBM J.Res.Develop.pp.364-377, sept.1971.
- /2/ -, "Microcoded modem transmitters" IBM J.Res.Develop.,pp.338-351, july 1974.
- /3/ P.J.Wan Gerwen, N.A.M.Verhoecks, H.A.Van Essen and F.A.M.Snijders, "Microprocessor implementation of high-speed data modems", IEEE Trans.Commun., vol. COM-25, pp.238-250, febr.1977.
- /4/ K.Watanabe, K.Inoue and Y.Sato, "A 4800 bit/s microprocessor data modem", IEEE Trans.Commun.vol.COM-26, No.5, pp. 493-498, may 1978.

/5/ R.W.Stroh, "An experimental microprocessor-implemented 4800 bit/s limited distance voice band PSK-modem", IEEE Trans. Commun. Vol.COM-26, pp.507-512, may 1978.

/6/ D.J.Nowak, P.E.Schmid, "A nonrecursive digital filter for data transmission", IEEE Trans.on Audio and Electroacustics, vol.AU-16, No.3, pp.343-349, sept.1968.

/7/ K.Murano, S.Unagami and T.Tsuda, "LSI Processor for digital signal processing and its application to 4800 bit/s modem", IEEE Trans.Commun.vol.COM-26, No.5, pp.499-506, may 1978.

Author: Asst. E.Borcoci, Address: Polytechnical Institute of Bucharest, Electronic Dept., Splaiul Independentei 313, 77748, Bucharest, ROMANIA.

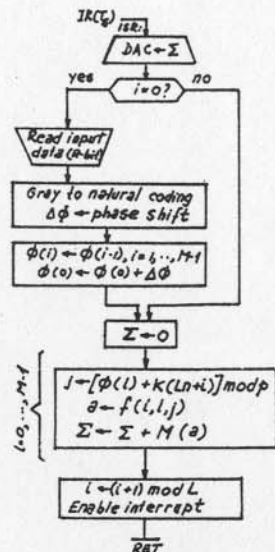


Fig.3. The cyclical processing program segment (PSK)

J. Vandewalle, H. De Man and J. Rabaey

ESAT LABORATORY, Katholieke Universiteit Leuven, Belgium.

A UNIFIED THEORY FOR THE COMPUTER AIDED ANALYSIS OF GENERAL SWITCHED CAPACITOR NETWORKS

ABSTRACT

We derive a general theory for the analysis of switched capacitor circuits. The circuits can have 2 phases or more and have no topologic constraints whatsoever. Also the inputs are arbitrary. We describe efficient computations of the time domain response, frequency response, noise and sensitivity analysis. All derivations are carried out in the modified nodal analysis framework, which allows an easy implementation in a computer program. The theory is simple and implies all previously described analysis techniques.

1. INTRODUCTION

Switched capacitor (SC) circuits have received much attention in recent years, because they allow to realize a filter with low sensitivity as an integrated circuit. Since these circuits are usually large, the analysis should be done by computer. Two types of computer implementations have been described [1-5], one based on time-domain analysis and one based on z-domain analysis. Here we present a unifying framework for both, which then also allows to evaluate their relative merits.

2. THEORETICAL ASPECTS OF THE ANALYSIS OF SC-NETWORKS

We consider arbitrary linear networks containing ideal switches, capacitors, independent voltage and charge sources and dependent sources VCVS, QCQS, The switches are controlled by Boolean clock variables $\phi_r(t) = 0$ or 1. $\phi_r(t) = 0$ (resp., $\phi_r(t) = 1$) corresponds to an open (resp., closed) switch at time t if this switch is driven by clock r . The time is partitioned into time slots $\Delta_k = (t_k, t_{k+1}]$ such that the clock signals do not vary in Δ_k , i.e. $\phi_r(t) = \phi_{rk}$ for $t \in \Delta_k$. We assume that the clock signals are T -periodic, with N time slots (called N phases) in one period of duration T .

Theorem 1 : If any above defined SC network is excited by a piecewise-constant excitation ($x(t) = x_m, y(t) = y_m, t \in \Delta_m$), then its response in the

$$\underline{V}_k(z) = \underline{H}_{kl}(z) \underline{U}_l(z) \quad (7)$$

It is shown in [6] that for any SC network \mathcal{N} and adjoint SC network $\tilde{\mathcal{N}}$ can be derived with a z-domain transfer matrix $\tilde{\underline{H}}$, whose columns correspond with rows of \underline{H} .

3. COMPUTATION OF FREQUENCY, NOISE AND SENSITIVITY PROPERTIES OF SC NETWORKS

Since a SC network is a time-varying circuit, a sinusoidal excitation may generate many frequencies in the output. However often one is only interested in the following practical frequency domain transfer function $\underline{H}(\omega)$ [1,3], which relates by definition the phasor \underline{U} of the sinusoidal excitation $\underline{u}(t) = \underline{U} e^{j\omega t}$ to the phasor at the same pulsation ω in the output $\underline{v}(t)$. Standard Fourier transform techniques allow to prove that

$$\underline{H}(\omega) = \sum_{k=1}^N e^{-j\omega t_{k+1}} v_k(\omega) \left(\sum_{l=1}^N e^{j\omega t_{l+1}} \underline{H}_{kl}(e^{j\omega T}) \right) + [(t_{k+1} - t_k)/T - v_k(\omega)] \underline{H}_{kk}(\omega), \quad (8.a)$$

where

$$v_k(\rho) = 2\{\sin[\rho(t_{k+1} - t_k)/2]\} \exp[j\rho(t_{k+1} - t_k)/2] / T\rho. \quad (8.b)$$

Similar expressions can be derived for the aliasing terms. In the computation of one entry (i,j) of (8.a) via the time-domain, N impulse responses have to be determined using (1) for impulse excitations in the N phases. The $H_{kl}^{(ij)}$ of (8.a) are then obtained via an FFT. In the z-domain we solve (5) once for $z = e^{j\omega T}$ and $\underline{U}_l = \underline{0}$, $U_l^{(n)} = 0$ for $n \neq j$ and $U_l^{(j)} = e^{j\omega t_{l+1}}$ for $l = 1, \dots, N$ in order to obtain the sums in brackets in (8.a).

In noise analysis one is usually interested in the output noise voltage at one node i due to the noise in many (say s) components. Let the vector $\underline{u}(t)$ of the input noise voltage sources be characterized by a given spectral density matrix $S_{\underline{u}}(\omega)$ which depends on the technology. Then the average spectral density function is given by

$$S_{v(i)}(\omega) = \sum_{n=-\infty}^{+\infty} \underline{X}^{(i.)}(\omega, \omega - n\frac{2\pi}{T}) S_{\underline{u}}(\omega - n\frac{2\pi}{T}) \underline{X}^{(i.)*}(\omega, \omega - n\frac{2\pi}{T}) \quad (9.a)$$

where

$$\underline{X}^{(i.)}(\omega, \Omega) = \sum_{l=1}^N e^{j\Omega t_{l+1}} \left(\sum_{k=1}^N v_k(\omega) e^{-j\omega t_{k+1}} \underline{K}_{lk}^{(i.)T} (e^{j\Omega T}) \right) + \sum_{k=1}^N [v_k(\omega - \Omega) - v_k(\omega)] e^{j(\Omega - \omega)t_{k+1}} \underline{K}_{kk}^{(i.)T}(\omega) \quad (9.b)$$

and * denotes complex conjugate and transpose and $\underline{K}_{lk}^{(i.)}$ with $l = N - l + 1$, $\tilde{k} = N - k + 1$ is the corresponding submatrix of the z-domain transfer matrix of the adjoint SC network and $\underline{A}^{(i.)}$ (resp., $\underline{A}^{(j.)}$) is the i-th row (resp., j-th column) of the matrix \underline{A} . In the time domain analysis all required $\underline{K}_{lk}^{(i.)}$ can be obtained from N impulse responses of the adjoint SC network and FFT's. In the z-domain analysis all required sums in brackets in (9.b) are obtained by solving for each frequency one set of equations (5) for the adjoint SC network with an appropriate excitation.

In sensitivity analysis one is interested in the effect of a variation in one or several components or the frequency on the input output relation. They can be computed by first deriving the sensitivity of an arbitrary entry $H_{kl}^{(ij)}$ of the z-domain transfer function (6) and then using these equations to compute the sensitivity of the frequency domain transfer function. $H^{(ij)}(\omega) = A^{(ij)}(\omega) e^{j\phi^{(ij)}(\omega)}$. We illustrate this by computing the group delay

$$T^{(ij)}(\omega) \triangleq \frac{\partial \phi^{(ij)}(\omega)}{\partial \omega} = \text{Im} \left(\frac{1}{H^{(ij)}(\omega)} \sum_{k=1}^N \frac{\partial H_k^{(ij)}(\omega)}{\partial \omega} \right) \quad (10.a)$$

where

$$\frac{\partial H_k^{(ij)}(\omega)}{\partial \omega} = \frac{[\omega(t_{k+1} - t_k) + j] e^{j\omega(t_{k+1} - t_k)} - j \sum_{l=1}^N e^{j\omega(t_{l+1} - t_{k+1})} H_{kl}^{(ij)}(e^{j\omega T}) - H_{kk}^{(ij)}(\omega)}{T\omega^2} + j v_k(\omega) \left\{ \sum_{l=1}^N e^{j\omega(t_{l+1} - t_{k+1})} [(t_{l+1} - t_{k+1}) H_{kl}^{(ij)}(e^{j\omega T}) + T e^{j\omega T} \frac{\partial H_{kl}^{(ij)}(z)}{\partial z} \Big|_{z=e^{j\omega T}}] \right\} \quad (10.b)$$

$$\frac{\partial H_{kl}^{(ij)}(z)}{\partial z} = -z^{-2} \underline{K}_{lk}^{(i.)T} \underline{A}_1 H_{Nl}^{(j)} \quad (10.c)$$

In order to obtain the group delay and all other sensitivities via the time domain, N impulse responses of the nominal and N impulse responses of the adjoint SC network and some FFT's have to be computed. In the z-domain ana-

lysis, (10.a) is obtained by solving three sets of equations (5) for the nominal SC network and one for the adjoint network.

4. CONCLUSION AND COMPARISON

We have described an elegant and general theory for the analysis of SC circuits from which the computer implementations via time domain or via z-domain are derived. Other analysis techniques [2-5] are either restricted to two phase SC networks [4-5] or require lengthy derivations [2-3] in order to obtain equations equivalent to (1) or (5). Although [2-3] may have less equations to solve, we are convinced that the use of macromodels in our framework can provide us with similar reductions. Moreover, the search for the minimal set of equations is largely academic, since sparse matrix methods have a computational complexity, which depends on the zero-nonzero pattern and not on the size of the matrix. It is important to observe that the use of the adjoint SC network [6] allowed great computational savings and hence it is equally useful in [2-5]. Let us conclude by comparing the time and z-domain technique. The time domain requires repeated solutions of (1), which is a smaller set of equations than (5), but it requires additional FFT's. The time domain analysis is implemented in DIANA [1] and progress is being made toward an implementation of the z-domain analysis.

REFERENCES

- [1] H. De Man, J. Rabaey, G. Arnout and J. Vandewalle, "Practical implementation of a general computer aided design technique for switched capacitor circuits", *IEEE J. of Solid State Circuits*, Vol. SC-15, April 1980, pp. 190-200.
- [2] F. Brglez, "SCOP, a switched-capacitor optimization program", *Proc. IEEE Intern. Symp. on Circuits and Systems*, Houston 1980, pp. 985-988.
- [3] Y.P. Tsvividis, "Analysis of switched capacitive networks", *IEEE Trans. on Circuits and Systems*, Vol. CAS-26, Nov. 1979, pp. 935-947.
- [4] C.F. Kurth and G.S. Moschytz, "Two port analysis of switched-capacitor networks using four-port equivalent circuits in the z-domain", *IEEE Trans. on Circuits and Systems*, Vol. CAS-26, March 1979, pp. 166-180.
- [5] Y.L. Kuo, M.L. Liou and J.W. Kasinskas, "Equivalent circuit approach to the computer-aided analysis of switched capacitor circuits", *IEEE Trans. on Circuits and Systems*, Vol. CAS-26, Sept. 1979, pp. 708-714.
- [6] J. Vandewalle, H. De Man and J. Rabaey, "The adjoint switched capacitor network and its applications", *IEEE Int. Symp. on Circuits and Systems*, Houston 1980, pp. 1031-1034.

ESAT Laboratory, K. Univ. Leuven
Kardinaal Mercierlaan 94
B-3030 Heverlee, Belgium

E. V. Sørensen
326, Institute of Circuit Theory and Telecommunication
Technical University of Denmark
DK-2800 Lyngby - Denmark

NORATOR-NULATOR FORMULATION OF THE NETWORK FUNCTIONS FOR SWITCHED-CAPACITOR CIRCUITS

ABSTRACT: A norator-nullator approach is used to express the z-domain network functions of switched capacitor circuits as a signed ratio of two determinants, i.e. in a form which permits the determination of poles and zeros by means of the well known two-sets-of-eigenvalues-approach. The involved nodal capacitance matrices are completely reduced with respect to all constraints imposed by closed switches, operational amplifiers and port-conditions. An algorithm is described, whereby these matrices can be compiled directly from the input circuit description.

1. Introduction

Recently several approaches to computer-aided analysis of switched-capacitor circuits have appeared [4][5][6] and undoubtedly many more will show up. The method described here is aimed at z-domain analysis of sampled-input, two-phase, equal time-slot, switched-capacitor two-ports consisting of capacitors, ideal switches and ideal operational amplifiers. It is based on Kurth's and Moschytz's fundamental nodal approach [1], and is related to Høkenek's and Moschytz's later computer-oriented method [5], but emphasizes and simplifies the algorithmic aspects in obtaining the necessary matrices. The theory is developed for two-phase switching schemes, but the extension to multiphase systems is obvious.

2. Universal formulas for switched-capacitor network functions

Fig. 1 shows the equivalent z-domain four-port [1] corresponding to a two-phase switched-capacitor two-port with equal time-slots A and B, and defines the port-node designations and sign-conventions adopted in the following. Except for the datum node: 0, every node k ($k=1,2,\dots,n$) in the physical two-port has an equivalent A-node: $k^A=k$ and an equivalent B-node: $k^B=k+n$ in the four-port.

Fig. 2 defines the transfer function $H^{BA}(z) = M^B(z)/G^A(z)$ with the generator and meter quantities: G^A and M^B being of type V or Q. Proper definition of H^{BA} requires that input-port B is short-circuited if G^A is a voltage, and that output port A is short-circuited if M^B is a charge. This is taken care of by the hypothetical switches SG and SM respectively. With these conditions satisfied H^{BA} may be found by nodal analysis of the equivalent two-port defined by ports p^A, q^A and r^B, s^B . Since similar definitions apply for H^{AA} , H^{AB} and H^{BB} we now drop the phase designation for H, pq and rs.

In order to obtain the reduced nodal z-domain capacitance matrix, let us first remove all closed switches and operational amplifiers from the equivalent two-port. This leaves an all-capacitive (possibly disconnected) structure with a $2n \cdot 2n$ nodal capacitance matrix $\underline{C}(z)$ of the form shown in Eq. 1:

$$\underline{C}(z) = \left[\begin{array}{c|c} \underline{C}_0 & z^{-1/2} \cdot (-\underline{C}_0) \\ \hline z^{-1/2} \cdot (-\underline{C}_0) & \underline{C}_0 \end{array} \right] \begin{array}{l} \left. \vphantom{\underline{C}(z)} \right\} A \\ \left. \vphantom{\underline{C}(z)} \right\} B \end{array} \quad (1)$$

where \underline{C}_0 is the simple real $n \times n$ nodal capacitor matrix of the correspondingly stripped physical circuit.

Next, let us model the short-circuits and op-amps by means of nullator-norator pairs as shown in Fig. 3, and reconstruct the equivalent two-port by