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ABSTRACT

A completely general method to calculate the exact frequency response of switched capacitor (s.c.) filters, including the effects of resistive timeconstants (e.g. due to switch resistances or bandlimited opamps) is presented. These effects are especially important in noise analysis or when designing high Q-filters. The method is based on the wellknown Modified Nodal Analysis (M.N.A.) description and is therefore completely compatible with traditional CAD-methods and with recently developed simulation programs for ideal s.c.-networks.

INTRODUCTION

Many approaches to computer aided analysis of ideal s.c. networks have been proposed [1-6]. None of them is able to handle the influence of bandlimited opamps and finite resistances on the frequency response in a general way. Work in this direction has been reported [7-9], but is based on state space or is an analytical method and limited to small networks.

However, the parasitic time constants are of prime interest when designing high Q-filters, filters with internal gain or with a small ratio between signal and sampling frequency or when performing noise calculations.

Therefore, a technique, based on the MNA-method, has been developed. The approach is an extension of the method, described in [5-6], suited for ideal s.c. networks and is completely general. No constraints are placed on the number and the length of the clockphases. Resistive and capacitive networks may be inserted and all frequency domain effects as $\sin(x)/x$ and continuous I/O-couplings are included.

The algorithm consists of one z-domain step, coupling the clockphases, and two AC-analyses, which take into account the internal frequency domain behaviour of the networks for each clock phase. The set up of an extended state transition matrix is necessary.

Realistic approximating techniques to handle this are presented. An example will be used to demonstrate the practical interest of the method.

DESCRIPTION OF THE ALGORITHM

Consider a switched capacitor network N_s^* , controlled by clocksignals, which are T-periodic, ie $\phi_i(t+T) = \phi_i(t) \forall t, i$ with ϕ_i a Boolean variable, denoting the high and low states of clock i. The time axis can be partitioned into time slots $\Delta_m = (tm, tm+1]$ such that the clock signals (and hence the network) do not vary in Δ_m . All time slots $\Delta_{k+\ell N}$ for $\ell=0,1,2,\dots$ with the same relative position k in a period (called phase k) have the same duration and the clock values are the same for $t \in \Delta_{k+\ell N}$.

For each phase k, the modified nodal equations can be set up :

$$\begin{bmatrix} G_k & A_k \\ B_k & D_k \end{bmatrix} \begin{bmatrix} v(t) \\ i(t) \end{bmatrix} + \begin{bmatrix} C_k & 0 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \dot{v}(t) \\ \dot{i}(t) \end{bmatrix} = \begin{bmatrix} j(t) \\ e(t) \end{bmatrix} \quad (1)$$

$$\text{or } G_{ke} \begin{bmatrix} v(t) \\ i(t) \end{bmatrix} + C_{ke} \begin{bmatrix} \dot{v}(t) \\ \dot{i}(t) \end{bmatrix} = \begin{bmatrix} j(t) \\ e(t) \end{bmatrix} \quad (2)$$

$v(t)$, $i(t)$, $e(t)$ and $j(t)$ respectively stand for the vectors of the nodal voltages, the currents in some selected branches, the voltage and current sources. Note also that in (1), the nodal matrix has been split up in C_k , the nodal capacitance, and G_k , the nodal conductance matrix. For sake of brevity we present the precise statements of the results as theorems and refer the reader for the proofs to the internal report [13].

Lemma 1 : The extended state transition matrix.

For each clockphase k, and for zero input signals, the values of the nodal voltages and selected branch currents at the end of clockphase k can be given in terms of the initial voltages and branch currents as expressed in

$$\begin{bmatrix} v(t_{k+1} + \ell T) \\ i(t_{k+1} + \ell T) \end{bmatrix} = P_k \cdot \begin{bmatrix} v(t_k + \ell T) \\ i(t_k + \ell T) \end{bmatrix} \quad \text{for } \ell=0,1,2,\dots(3)$$

with $P_k = Y_k(t_{k+1} + \ell T)$ where $Y_k(t)$ is the extended state transition matrix, which is by definition the solution of differential equation (4) with C_{ke} and G_{ke} as defined in (2).

$$C_{ke} \cdot \dot{Y}_k(t) + G_{ke} \cdot Y_k(t) = 0 \quad (4)$$

General Solution (non-equilibrium).

Matrix exponential-approximating techniques, which can handle singular matrices, can be used to solve the differential equation (4). Among the many existing techniques [12], the Padé approximation seems to be best suited for this application: e.g. for first order Padé approximation (or trapezoidal)

$$P_k \approx \left[\left[C_{ke} + \frac{h}{2} G_{ke} \right]^{-1} \cdot \left[C_{ke} - \frac{h}{2} G_{ke} \right] \right]^m \text{ with } h = \frac{\Delta k}{m} = \frac{t_{k+1} - t_k}{m} \quad (11)$$

This expression is always computable, due to the fact that $C_{ke} + \frac{h}{2} G_{ke}$ is non-singular. When choosing m high enough to ensure accuracy for the dominant time-constants, first or higher order Padé-approximations lead to a stable and accurate algorithm to approximate P_k .

Computation of P_k using the equilibrium principle.

In most of the commonly used sc-systems, it may be assumed that the transient effects, which occur after each switching instant, disappear completely before the end of the particular timeslot and that the different node voltages reach their equilibrium (under zero input conditions). The basic philosophy is to invoke this equilibrium in order to assume that at the end of the timeslot the capacitor voltages do not vary. So they can be replaced by open circuits. The remaining equations, which describe the resistive networks in equilibrium, are not sufficient. They have to be completed with a set of capacitor charge equations, leading to a complete set of equations, describing the equilibrium state. These capacitor equations can be set up using a topological approach, which uses the same ideas as these of [11].

Consider a s.c.-network N_s , which may contain ideal and non-ideal switches, capacitors, resistors, current and voltage sources and all controlled sources. No cutsets of capacitors and current sources are allowed. A two step algorithm is presented for the computation of P_k of phase k .
STEP 1: Consider all cutsets of N_{sk} (N_s for phase k), containing only capacitors and open switches. These cutsets divide N_{sk} in ℓ different parts, which are called N_{sk}^i and which contain n_i nodes, ($i=1, \dots, \ell$).

A complete independent set of such cutsets can be found by inspecting the non reduced incidence matrix, and rearranging rows and columns. Rearranging the MNA equations in the same way, grouping the nodes of N_{sk}^i ($i=1, \dots$) a new set of equations is obtained:

$$\begin{bmatrix} C_k & 0 \\ 0 & 0 \end{bmatrix} \cdot \begin{bmatrix} \dot{v}' \\ \dot{i}' \end{bmatrix} + \begin{bmatrix} G_k & A_k \\ B_k & D_k \end{bmatrix} \cdot \begin{bmatrix} v' \\ i' \end{bmatrix} = \begin{bmatrix} j' \\ e \end{bmatrix} \quad (12)$$

STEP 2: Removal of the dependent equations when the equilibrium principle is valid.

Consider the Kirchoff current equations of (13) which are related to the n_i nodes inside the sub-network N_{sk}^i

$$C_{ki}' \dot{v}'(t) + G_{ki}' v'(t) + A_{ki}' i'(t) = j_i'(t) \quad (13)$$

Choose a reference node for each N_{sk}^i (suppose we choose the last node), except for the subnetwork containing the ground reference node. Dropping the Kirchoff current equation of the reference node of N_{sk}^i and replacing it by the sum of all Kirchoff current equations of N_{sk}^i (Note that this equation is purely capacitive, by the choice of the cutsets), (13) is equivalent with (14-15):

$$n_i - 1 \{ C_{ki}'' \dot{v}'(t) + G_{ki}'' v'(t) + A_{ki}'' i'(t) = j_i'(t) \} \quad (14)$$

$$1 \{ C_{ki}''' \dot{v}'(t) = 0 \} \quad (15)$$

$$\text{with } C_{ki}'''(1, m) = \sum_{\ell=1}^{n_i} C_{ki}'(\ell, m) \text{ for } m=1, \dots, n_i$$

$$\text{and } C_{ki}''(\ell, m) = C_{ki}'(\ell, m) \text{ for } m=1, \dots, n_i \text{ and } \ell=1, \dots, n_i - 1$$

Integrating equation (15), which is precisely the cutset equation and supposing that the equilibrium principle is valid, which implies that all node voltages at the end of the timeslot are constant for zero inputs or $\dot{v}'(t_{k+1} + \ell T) = 0$, equations (14) and (15) are transformed into (16) and (17)

$$G_{ki}'' v'(t_{k+1} + \ell T) + A_{ki}'' i'(t_{k+1} + \ell T) = 0 \quad (16)$$

$$C_{ki}''' v'(t_{k+1} + \ell T) = C_{ki}''' v'(t_k + \ell T) \quad (17)$$

By the choice of the cutsets and the setup of the equations, it is clear that (16) and (17) give an independent set of equations. Repeating this for all N_{sk}^i , combining equations (16), (17) and the voltage equations of (12), leads to (18)

$$\begin{bmatrix} C_k'' & A_k'' \\ C_k''' & 0 \\ B_k' & D_k' \end{bmatrix} \begin{bmatrix} v'(t_{k+1} + \ell T) \\ i'(t_{k+1} + \ell T) \end{bmatrix} = \begin{bmatrix} 0 \\ C_k''' \\ 0 \end{bmatrix} v'(t_k + \ell T) \quad (18)$$

or

$$P_k = \begin{bmatrix} C_k'' & A_k'' \\ C_k''' & 0 \\ B_k' & D_k' \end{bmatrix}^{-1} \cdot \begin{bmatrix} 0 \\ C_k''' \\ 0 \end{bmatrix} \quad (19)$$

Following these steps, an algorithm can be set up easily. It must be emphasized that this equilibrium principle is valid for almost all practical sc-circuits and is interesting for the calculation of noise transferfunctions, high Q filters,...

EXAMPLE

The frequency response of the high Q ($Q=100$)-biquad bandpass filter of Fig. 1 is shown in Fig. 2. ($f_s=200\text{Khz}$, $f_0=12.7\text{Khz}$). Fig. 2 gives the frequency domain response of node V_{out2} . Curves a&b respectively show the responses with ideal opamps and opamps with limited gain (10K), while curve c is the response with non-ideal opamps ($f_p=100\text{Hz}$, $A=10\text{K}$). The large deviation of V_{out2} (c) is caused by the huge loading of opamp2 ($Q_L=78.5$). This simulation was realised with a special test program, `mt` using sparse matrix techniques.

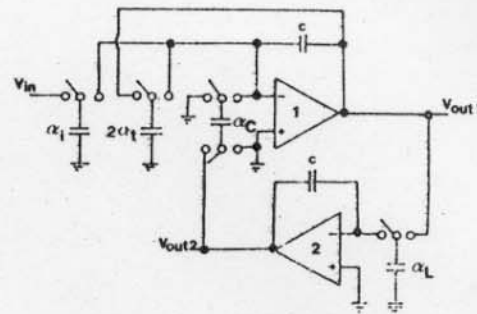
CONCLUSIONS

A completely general method for the analysis of switched capacitor systems in the frequency domain has been presented. The effects of all non-idealities as limited opamp-bandwidths, resistances, continuous I/O couplings, resistive dividers and time constants, ... can be studied.

The presented method is completely compatible with existing CAD-methods for ideal sc networks and ac analysis and is particularly suited for noise analysis of sc systems.

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f_s : 200 KHz
 opamp-gain : 10K
 Unity-gain B.W. : 1 Mhz
 α_L : 78.5
 $\alpha_i \alpha_t \alpha_c$: 0.00198

FIG. 1

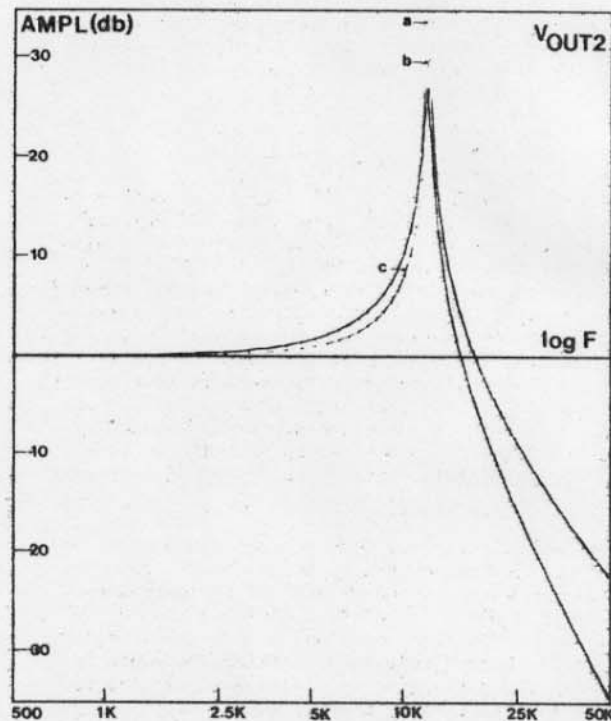


Fig 2

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