

# Dejan Markovic

Berkeley Wireless Research Center  
2018 Allston Way, Suite 200  
Berkeley, CA 94704

Email: [dejan@eecs.berkeley.edu](mailto:dejan@eecs.berkeley.edu)  
URL: [http://bwrc.eecs.berkeley.edu/People/Grad\\_Students/dejan](http://bwrc.eecs.berkeley.edu/People/Grad_Students/dejan)

Work Phone: (510) 666-3124  
Mobile Phone: (510) 612-2998  
Fax: (510) 883-0270

---

## EDUCATION: University of California, Berkeley, CA

Ph.D. in Electrical Engineering and Computer Sciences, Expected March 2006.

M.S. in Electrical Engineering and Computer Sciences, 2000.

## University of Belgrade, Yugoslavia, School of Electrical Engineering

B.S. in Electrical Engineering, 1998. GPA: 9.6/10.0 (top 1%)

## ACADEMIC EXPERIENCE:

- 8/98 – present **University of California, Berkeley, Berkeley Wireless Research Center**, Graduate Student Researcher.  
Research: energy-efficient digital ICs and architectures for multi-antenna wireless communications.
- 08/05-12/05 **University of California, Berkeley, Dept of EECS**, Course Instructor. (80 students)  
Instructor for “EE141: Digital Integrated Circuits,” an upper-division course that includes weekly homework assignments, bi-weekly hardware labs, and two multi-person design projects on digital integrated circuits. Delivered lectures and managed assignments, labs, and projects with help from three teaching assistants.
- 8/99 – 5/01 **National Technological University, Berkeley, CA, UC Berkeley Branch**, Course Consultant.  
Facilitated graduate-level classes to industry engineers and college students, based upon previously videotaped courses at UC Berkeley. Responsibilities include formulating and grading homeworks, project, and exams, and assigning final grades. Consulted for the following courses:  
“IC771CA (EECS240): Advanced Analog Integrated Circuits”, two semesters,  
“IC742CA (EECS241): Advanced Digital Integrated Circuits”, four semesters.
- 3/98 – 7/98 **University of California, Davis, Advanced Computer System Laboratory**, Research Scholar.  
Conducted research in the area of pass-transistor logic. (undergraduate diploma project)

## WORK EXPERIENCE:

- 5/01 – 8/01 **Intel Corp., Hillsboro, OR**  
*Circuits Research Lab / Microprocessor Research Lab*, Summer Intern.  
Designed low-energy flip-flops for dual-supply microprocessors. Two patents granted.
- 6/99 – 8/99 **Ernest Orlando Lawrence Berkeley National Laboratory, Berkeley, CA**  
*Integrated Systems Group*, Summer Intern.  
Designed parts of pixel-array IC for X-ray spectroscopy. Functional chip, co-authored two papers.

## HONORS & AWARDS:

- Intel Foundation Fellowship, academic year 2003-2004.
- CalVIEW International Fellow Award 2002-2003, for excellence in facilitating distant learning program to the University of Philippines at Dulles, Summer 2002.
- Hitachi’s Mu-Chip Fellowship 2002, market research on Hitachi’s technology for radio-identification.
- CalVIEW Fellow Award 2001-2002, for excellence in teaching industry engineers.
- First place, team and individually, areas: Analog ICs, Digital ICs, Elekrijada (National Competition of Schools of Electrical Engineering), H. Novi, Yugoslavia, 5/97.

## MEMBERSHIPS AND OTHER ACTIVITIES:

- IEEE Student Member (11/96 – present)
- Reviewer for the IEEE Transactions on Very Large Scale Integration (VLSI) Systems.
- Reviewer for the IEEE Transactions on Circuits and Systems (TCAS-I).
- UC Berkeley Digital Circuits Design Group (DCDG), Group leader, 2003.

## LIST OF PUBLICATIONS:

[http://bwrc.eecs.berkeley.edu/People/Grad\\_Students/dejan/papers/papers.htm](http://bwrc.eecs.berkeley.edu/People/Grad_Students/dejan/papers/papers.htm)