

## HUIFANG QIN

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[http://bwrc.eecs.berkeley.edu/People/Grad\\_Students/huifangq/main.htm](http://bwrc.eecs.berkeley.edu/People/Grad_Students/huifangq/main.htm)

### EDUCATION

**University of California at Berkeley** **Berkeley, CA**  
Ph.D. in Electrical Engineering, Expected graduation: 2006  
**Tsinghua University** **Beijing, China**  
B.S. Electrical Engineering, May 2000

### CAREER INTERESTS

- **Entrepreneurship:** Providing effective wireless solutions for modern society
- **Research:** Robust ultra-low power VLSI design for wireless communication system

### EXPERIENCE

**1/01-Present** **Graduate Student Researcher, University of California, Berkeley** **Berkeley, CA**  
Berkeley Wireless Research Center, Advisor: Professor Jan M. Rabaey.

- Leader of the Yield Optimization Design Approaches (YODA) research group
- PhD work focuses on developing design strategies to achieve ultra-low power consumption and robust functionality for emerging ambient intelligence applications such as high-density and low-cost wireless sensor network, wearable electronics and personal communication systems.

**6/04-9/04** **Research Intern, STMicroelectronics Berkeley Lab** **Berkeley, CA**  
Energy-efficient DSP processor design research group, Manager: Bhusan Gupta

- Worked with a group of ten experienced VLSI design engineers to develop an automatic power analysis flow for a DSP processor designed in latest semiconductor technology.

**6/03-9/03** **IBM T. J. Watson research center** **Yorktown, NY**  
Technology and Design Integration Group, Mentors: Kerry Bernstein and Edward Nowak

- Designed VLSI memory circuits in a novel 65nm semiconductor technology with FinFETs.
- Closely worked with nine IBM research scientists located in five research centers worldwide. The job was highly evaluated for the capability to expand the team and effectively communicate.

### TEACHING

**1/04-5/05** **CalVIEW Course Consultant, National Technological University**  
**1/03-5/03** **Graduate Student Instructor, EECS, University of California, CA**

### HONORS

- **Mayfield Fellowship**, 2004-2005
- UC Berkeley MOT China Fellowship, 2004-2005
- **Best Paper Award** at the International Symposium on Quality Electronic Design, ISQED 2004
- UC Regents Fellowship at University of California, Berkeley, 2000 ñ 2001
- Scholarship for Exceptional Academic Performance at Tsinghua University, China, 1999-2000

### LANGUAGES

Proficient in English and Mandarin Chinese

### INTERESTS

Tennis, running, snowboarding, travel, Karaoke, cooking